CURRICULAR STRUCTURE FOR PART- II (2ND YEAR) OF THE FULL TIME DIPLOMA COURSES IN ENGINEERING AND TECHNOLOGY

	WEST	BENGAL	STA	ГЕ СС	OUNC	IL OF	TEC	HNICAL I	EDUCA	ATION		
T	TEACHING AND EXAMINATION SCHEME FOR DIPLOMA IN ENGINEERING COURSES											
COU	RSE NAME: DIPI	LOMA (3 Y	EAR	LS)								
DUR	ATION OF COUR	RES: 6 SEM	EST	ERS								
	ESTER: FOURTH											
BRA	NCH: ELECTRO	NICS AND				TATI	ON E					
			P	ERIOI	DS			EVALU	ATION	SCHE	EME	
SR.	SUBJECT	CREDIT						RNAL		Р	'R	
NO.	BODILET	CILLDII	L	TU	PR		SCHI	r	ESE			TOTAL
						TA	СТ	TOTAL		INT	EXT	
1	Analog Electronics	3+2	3		3	10	20	30	70	50	50	200
2	Digital Electronics	4+2	3	1	3	10	20	30	70	50	50	200
3	Process Instrumentation – I	4+2	3	1	3	10	20	30	70	50	50	200
4	Electronic Measuring Instruments	2	2			5	10	15	35			50
5	Process Control	4	3	1		10	20	30	70			100
6	Development of Life skill- II	1			2					25	25	50
7	Professional Practice - II	1			2					50		50
	TOTAL 25 14 3 13 45 90 135 315 400 850										850	
	DENT CONTACT											
	Theory and Practical Period of 60 Minutes each.											
	L - Lecture, TU – Tutorial, PR- Practical, TA- Teachers Assessment, CT- Class Test, ESE – End Semester Exam, INT – Internal, EXT- External											
Seme	ster Exam, INT –	Internal, E2	X I - I	Extern	ai							

Syllabus for ANALOG ELECTRONICS

		e : Diploma in Electronics & Instrumentation	n Engineering	
		: Analog Electronics		
Subject C			emester: Third	
Duration:	6 months	Ma	aximum Marks: 100	
Teaching			camination Scheme:	
	y : 3hrs	week	Internal Scheme : Teachers Assessment: 1	
Tutori				20
Practi	cal :		End Semester Exam :	70
Credit: 3				
Aim:				
Sl No.				
1		ubject intends to teach operating principle an		ces like
		ent types of amplifiers, oscillators and their a		
2		bject knowledge is required in Industrial Ele		
3		standing of the subject will provide the stude s & devices	ent for assembling, trouble shooting & testin	g of
	ciicui	s & devices		
Objective	:			
Sl No.		tudent will able to		
1		per biasing for transistor		
2		y and explain various amplifiers and Oscillat		
3	under	stand the operation and application of different	ential amplifier and operational amplifier	
Pre-requ	isita:			
Sl No.	15110.			
1	Basic	Electronic Engineering		
2	Basic	on different active and passive components		
0		Contents		TT
Group	Module	Name of t	the topic	Hrs / Module
	1	Transistor Biasing		9
		1.1. Idea on Eaithful amplification stabilized	action	
		1.1 Idea on Faithful amplification, stabilisa1.2 Need for transistor biasing, stability factor		
		Q point	actor, concept of de toad mile, selection of	
		1.3 Different methods of transistor biasing	g (base resistor/ fixed bias, emitter bias,	
		collector feedback bias, self bias) with	n stability analysis.	
		1.4 Thermal run away & its prevention, he	eat sinks.	
	2			11
A	2	Small Signal & Power Amplifier		11
		2.1 Small Signal Amplifier		
			e stage amplifier, phase reversal, DC &	
		AC equivalent circuit, Load line.		
		2.1.2 Analysis, Classification of amplifie		
			port network, hybrid parameter model for	
			E amplifier using hybrid parameter.	
		2.1.4 Operation of difference amplifier2.1.5 Operation of darlington amplifier		
		2.1.5 Operation of darnington amplifier		

		2.21 2.22	Operation, r	er s A, B, AB & nerits & demo cross over dis	erits of Class					
В	3	 Feedback Amplifier and Oscillator 3.1 Concept of Positive & negative feedback. 3.2 Amplifier without & with feed back. 3.3 Advantage of negative feed back on voltage gain, bandwidth, input output impedance, stability, noise, distortion. 3.4 Classification of oscillators, principle of oscillation, damped & un-damped oscillation, use of positive feed back, Barkhausen criterion for oscillation 3.5 Different oscillator circuits: L-C tuned collector oscillator, R-C phase shift oscillator, Wien bridge oscillator. (operation, frequency of oscillation of those) 								
	4	Operational Amplifier134.1 Op-amp configurations (building blocks), op-amp parameters, characteristics of an ideal op-amp.134.2 Examples of IC op-amp4.3 Application of op-amp as inverting amplifier, non-inverting amplifier, adder, subtractor, differentiator, integrator, unity gain buffer, V to I & I to V converter, comparator, Regenerative comparator (Schmitt trigger), Square wave generator (Astable multivibrator), sample & hold circuit, instrumentation amplifier, Charge Amplifier, High Pass & Low Pass filter.13								
Books:										
Title				Author				Publisher		
	c Devices &	& Circuit	Theory		& Nashelsky			PHI Pub. Co.		
	c Principles		2	A.P. Malvi				Mc Graw Hill		
	of Electror				, Rohit Meh	ta		S Chand		
<u> </u>	nal Amplifi		ear ICs	D. A. Bell	,			Oxford University	Press	
	c Devices &			D. A. Bell				Oxford University		
	d Electronio		-	Millman &	Halkias			Mc Graw Hill		
	& linear in		circuits	R. Gayakw				PHI		
	on of Electi				ayay, Raksh	it. Saha Pr		New Age		
	ntegrated C		sign	Johns		.,		Wiley India		
•	tegrated Ci		0		ou, Suseela I	3.		Scitech Publicatio	n	
	tegrated Ci			D Roy Cho				New Age		
	c Device &			Mottershed				PHI		
			E	nd Semester	Examinatio	on Scheme				
		Maxim	um Marks: 7	70			Tit	ne: 3 Hrs		
Crown										
Group	Module	Taba	To be	ve Questions	Total	Taba	To be	ive Questions	Total	
	woadle	To be		Marks per question	l otal Marks	To be		Marks per	I otal Marks	
		set	answered	question	IVIALKS	set	answered	question	IVIAIKS	
А	1	10				3	Any 5			
	2				1 x 20		taking at		10 x 5	
			Any 20	1	=20		least 2	10	=50	
В	3	15			-20	4	from each	1	-50	

from each group

Syllabus for DIGITAL ELECTRONICS

		e : Diploma in Electronics & Instrumentation Engineering	
	3	t : Digital Electronics	
Subject		Semester: Third	
	n: 6 months		
	g Scheme:	Examination Scheme:	
	ory : 3hrs/		
	orial : 1hrs		: 20
Prac	tical :	End Semester Exam	: 70
Credit:	4		
Aim:	-		
Sl No.			
1	To study	different logic families	
2		luce different logic gates, Boolean algebra & combinational logic design using those	gates
3		how to design sequential logic using flip flop	0
Objecti	ve:		
Sl No.		ent will able to	
1		imple logic circuits	
2		e logic circuits	
3		outputs of logic circuits	
4	Troubles	hoot digital circuits	
D -10 -10 0			
Pre-reg	Basic Ph	veice	
2		cs Engineering	
2	Licetion		
		Contents	
Group	Module	Name of the topic	Hrs / Module
	01	Number Systems & Code:	6
		 Binary, octal, decimal & hexadecimal number systems, conversion from one system to another system. Binary arithmetic, signed numbers, subtraction using 1's & 2's complement representation, concept of over flow. Code- BCD, alphanumeric, ASCII. Concept of parity & error correction. 	
А	02	Boolean Algebra & Logic Gates:	9
		 2.1 Logical symbol, logical expression and truth table of AND, OR, NOT, NOR, NAND, XOR, XNOR, 2.2 Use of NAND & NOR gates as universal gates, implementation of Boolean functions using logic gates, circuit of logic gates using discrete components. 2.3 Digital logic families- RTL, TTL & CMOS, characteristics & circuit configurations, open collector & tri state gates 	

	03	Combinational Logic Circuits:	11
		 3.1 Boolean functions & standard canonical forms, simplification using Boolean algebra & K- map, Minimization of logical expression using K- map (2, 3, 4 variables), POS & SOP 3.2 Truth Table, K –map, simplified logical expression and logical circuit of Half Adder & Full Adder, Half Subtractor & Full Subtractor. 3.3 Block diagram and Truth Table of Encoders, priority Encoder ICs and Decoder, Decoder-Driver 3.4 Block diagram, Truth Table, working principle, Application, PIN functions of Decimal to BCD Encoder (IC 74147) and BCD to & Segment Decoder 	
	04	Multiplexers & Demultiplexers:	7
		 4.1 Block Diagram, Truth Table, Logical Expression and Logic Diagram of Multiplexer (4:1 and 8:1), Design of 8:1 using 2 4: 1 Mux, Multiplexer ICs 4.2 Block Diagram, Truth Table, Logical Expression and Logic Diagram of Demultiplexer (1:4, 1:8, 1:16), Demultiplex ICs 	
	05	Flip Flops & Sequential Logic Design:	13
В		 5.1 Symbol, logic diagram using NAND/NOR gates , working and Truth Table of S R flip flop 5.2 Symbol, logic diagram using NAND gates , working, Truth Table and timing diagram of clocked S R flip flop 5.3 Concept of edge triggering, level triggering 5.4 Symbol, logic diagram using NAND gates , working, Truth Table and timing diagram of clocked J K flip flop 5.5 Block diagram and Truth Table of Master-slave J K flip flop 5.6 Symbol, working and Truth Table of D & T flip flop 5.7 Application of flip flops 5.8 Working, truth table, & timing diagram of Synchronous Counters (3 bit), Asynchronous Counter (3 bit, 4 bit), Mod N Counter, synchronous, up, down, up down, binary, decade, divide by N, ring counter. 5.9 Block diagram, Working, Truth Table and waveform of Shift registers- SISO, SIPO, PISO, PIPO (4 bit) and universal Shift registor. 5.10 Application of Counter & Registor 	
	06	A/D & D/A Converters:	9
		 6.1 Block diagram and working of Ramp type, dual slope type, SAR type Flash type A/D converter, 6.2 Specifications of A/D converter. 6.3 Circuit diagram and working of R-2R ladder DAC & Weighted resistor D/A converter, Their comparison. 6.4 Specifications of D/A converter 	
	07	Timing Circuits:	5
		7.1 Block diagram & Pin diagram of 555 IC Timer7.2 Working of 555 timer as astable multivibrator7.3 Working of 555 times as monostable multivibrator	

Books:										
Title				Author]	Publisher		
Digital P				R.P. Jain]	Mc Graw Hill		
Fundame	ntals of Di	gital Circ	uits	A Anand k	Kumar]	PHI		
U	lectronics			A.K. Main				Wiley India		
Digital E	lectronics			G K Khara	te			Oxford Universit		
Digital C	ircuits & S	ystems		Hall]	Mc Graw Hill Pu	ıb. Co	
	rinciples &		ions	Malvino &]	Mc Graw Hill Pu	ıb. Co	
	ystem Desi			Morris Ma	-]	PHI		
	g Theory &	: Logic D	esign	C. V. S. Ra	10			Pearson		
	lectronics			S. P. Bali				Mc Graw Hill		
	ircuit -Vol			D. Ray Ch				Platinum Publish	-	
U	ircuit -Vol			-	D. Ray Chaudhury			Platinum Publish	er	
	ntegrated El				H. Taub, D. Shilling				Mc Graw Hill Pearson	
•	undamental	ls			Floyed, Jain					
0	lectronics			P. Raja					Scitech	
U	ircuit & De	<u> </u>		S. Aligahanan, S. Aribazhagan				Bikash Publishing		
A Text B	ook of digi	tal Electr		R S Shedda S Chand & Company nd Semester Examination Scheme					pany Ltd	
			Ε	nd Semester	Examinati	ion Schem	e			
		Maxim	um Marks: 7	0			Time	3 Hrs		
			Objectiv	e Questions			Subjective	Questions		
Group	Module	To be	To be	Marks per	Total	To be	To be	Marks per	Total	
		set	answered	question	Marks	set	answered	question	Marks	
А	1	12				3				
	2	12				5				
3			1 x 20		Any 5 taking		10 x 5			
	4		Any 20	1	=20		at least 2 from	n 10	=50	
В	5	13			-20	5	each group		-50	
	6									
l	7									

Syllabus for PROCESS INSTRUMENTATION - I

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Name of the Subject : Process Instrumentation Subject Code:	Semester: Third		
Duration: 6 months Maximum Marks: 100			
Teaching Scheme:	Examination Scheme:		
Theory : 3hrs/week	Internal Scheme : Teachers Assessment: 10		
Tutorial : 1hrs/week	Class Test : 20		
Practical :	End Semester Exam : 70		
Credit: 4			
Aim:			
Sl No.			
	ecise measurement of those is the most important part The subject and techniques of process parameters like force, pressure etc		

	To study s	signal conditioning, signal transmission, and storage for analysis & control							
Objective	e:								
Sl No.	The Stude	ent will able to							
1	Know the pressure, s	principle of operation, advantages, disadvantages of different process parameter like fo strain etc	rce,						
2	Choose th	e proper measuring sensor/ instrument for specific measurement							
3	Compare the different instruments used for a specific operation								
4		specification of different measuring instruments							
5	Identify si control	ignal conditioning circuit for Instrumentation system in Industrial process, measurement	t &						
6		ic idea on signal transmission in Instrumentation system							
7		principle and application of different proximity sensors							
Pre-requ	isite:								
Sl No.									
1		ntal idea of Instrumentation system							
2		electronics							
3	Basics of								
Module	Chantan	Contents Nome of the tonics	Hrs /						
Module	Chapter	Name of the topics	Module						
	1	Proximity Sensors:	7						
	1	 1.1 Definition and importance of proximity sensors in instrumentation field 1.2 Description & application of different types of proximity sensors such as Inductive, optical, magnetic, capacitive, ultrasonic types 							
	2	Strain Gauge & Load Cell:	9						
А		 2.1 Material of construction of strain gauge, classification, gauge factor, strain measuring circuit, dummy strain gauge, advantage, disadvantage and application of various types of strain gauge, advantage of semiconductor strain gauge over metallic strain gauge. 2.2 Load cell- column type, shear type, application of load cells in industries. 							
Λ	3	Pressure Measurement:	12						
		 3.1 Definition, classification & units of pressure. Relation between different units 3.2 Manometer : U tube, well type & micrometer 3.3 Elastic pressure sensor: bourdon tube, bellow, diaphragm, capsule. 3.4 Vacuum measurement using Mc Loed gauge, Thermal conductivity gauge, Ionization gauge, Pirani gauge 3.5 Dead Weight Tester 3.6 Calibration of pressure measuring instruments by manometer & dead weight tester 3.7 Flapper- Nozzle system for pressure measurement 							
_	4	Signal Conditioning	11						
В		 4.1 Relevance of signal conditioning in process instrumentations. 4.2 Principles of analog signal conditioning. Brief idea on signal level & bias changing, linearization, conversions, filtering & impedance matching. Concept of loading 4.3 Passive signal conditioner: voltage divider, Wheatstone bridge circuit, RC filter 4.4 Active signal conditioner: Idea on op amp based circuit 4.5 I/P, P/I, I/V, V/I, F/V, V/F converters. 4.6 Instrumentation Amplifier, Charge amplifier, & their importance, 							
	5	Data Processing	11						
	5	Data Processing	11						

		 5.2 Block diagram and e 5.3 Objective of DAS 5.4 Signal Conditioning conversion 5.5 Single channel and m 	ocessing in Instrumentation explanation of a generalised Data Action DAS, Radiometric conversion, L nultichannel DAS mation, characteristics of Data Logg	ogarithmic			
		5.7 Comparison betweer					
	6	Data Transmission & Teler	netrv	10			
	6.1 Idea on pneumatic & electric transmission6.2 General Telemetry system						
Books:		 6.4 4-20 mA current tran 6.5 Need for modulation 6.6 Basic idea on amplit 6.7 Need of Multiplexer 6.8 Idea on analog and d 	lvantage of digital data transmission	eduction) f TDM & FDM a over analog data			
Title			Author	Publisher			
		surement & Instrumentation	Ghosh	PHI			
		ial Instrumentation	D.Patranabis	TMH			
Measurement System Application & Design			E.O.Doeblin	Mc Graw Hill			
		s' Handbook, Vol I: Process	Bela G. Liptak	CRC Press, Taylor &			
Measuremen		7		Francis			
Instrument T			H K P Neubert	Oxford University Press			
Mechanical The Ferrer			Beckwith, Buck & Marangoni	Narosa Pub. House			
The Essence			Allan Morris	PHI S.K.K.t. i. D.L.C.			

A Course in Electrical & Electronics	J.B. Gupta	S. K. Kataria Pub. Co.
Measurement & Instrumentation		
Industrial Instrumentation & Control	S K Singh	ТМН
Sensors & Transducers	D. V. S. Murty	PHI
Instrumentation & Control	Reddy, P S R Krishnudu	Scitech
Handbook of Modern Sensors	Fraden, Jacob	Springer

	End Semester Examination Scheme											
		Maxim	um Marks: 7	70			Time	: 3 Hrs				
Group			Objectiv	ve Questions			Subjective	e Questions				
	Module	To be	To be	Marks per	Total	To be	To be	Marks per	Total			
		set	answered	question	Marks	set	answered	question	Marks			
А	1 2	12				4	Any 5 taking at					
	3		Any 20	1	1 x 20		least 2	10	10 x 5			
В	4	13	7 my 20	1	=20	4	from each	10	=50			
	5	15				-7	group					
	6											

Syllabus for ELECTRONIC MEASURING INSTRUMENTS

Name of	the Course	: Diploma in Electronics & Instrumentation Engineering	
Name of	the Subject	: Electronic Measuring Instruments	
Course C	ode:	Semester: Third	
Duration	: 6 months	Maximum Marks: 50	
Teaching	Scheme:	Examination Scheme:	
	ry : 2 hrs/		ment : 05
Tutor		Class Test	: 10
Pract	ical :	End Semester Exam	: 35
Credit: 2			
Aim:			
Sl No.			
1	The subje	ct knowledge is required to provide detail knowledge for proper handling of electronic	
	instrumen	ts which are widely used in electronics laboratory & electronics workshops.	
2	Understar	nding the subject will provide skill to the students for trouble shooting & testing of circ	cuits using
	instrumen	ts	
Objectiv			
Sl No.		ent will able to	
1		ferent analog and digital measuring instruments	
2 3		but the digital frequency meter	
<u> </u>		ne operation of CRO and Function Generator a regarding the maintenance & service of these instruments	
4	get all lue	a regarding the mannenance & service of these first unlefts	
Pre-requ	usite:		
Sl No.			
1	Idea on ba	asic analog and digital electronics	
2	Idea on ac	ctive and passive components	
		Constants	
Group	Module	Contents Name of the topic	Hrs/
Group	mouule	Traine of the topic	Module
	1	Different Measuring Instrument	10
		1.1 Comparison between analog & digital measuring instrument	
		1.2 Rectifier type, True RMS type analog voltmeter.	
		1.3 Ramp type and dual slope integrating type DVM	
		1.4 Q Meter- application & error.	
А	2	Frequency & Power Measurement	5
		2.1 Name of different frequency meter	
		2.2 Operation of Electronic frequency counter for the measurement of	
		frequency and time period.	
		2.3 Power Measurement by bolometer & calorimetric method	

	3	CRO and its Application:	10
		3.1 Block diagram of CRO	
		3.2 Cathode Ray Tube, Deflection Amplifier, Time base generator, Delay line.	
		3.3 Electrostatic deflection technique. Automatic synchronization of time base.	
		3.4 Basic control of CRO	
		3.5 Different types of probes.	
		3.6 Dual trace, dual beam CRO.	
В		3.7 Measurement of time period, phase, delay time, frequency by CRO.	
	4	Signal Generators:	5
		4.1 AF sine and square wave generator	
		4.2 Function generator	
		4.3 Pulse generator.	
		4.4 Sweep Generator	
		•	•

Books:					
Title	Author	Publisher			
Electronic Measurements	D.A.Bell	PHI			
A course in Electrical & Electronics Measurement &	A.K. Sawhney	Dhanpat Rai Pub. Co			
Instrumentation					
Electronic Instrumentation	Kalsi	Mc Graw Hill			
Elements of Electronic Instrumentation & Measurement	J. J. Car	Pearson			
Modern Electronic Instrumentation & Measurement	Helfrick & Cooper	PHI			
Techniques	_				
A Course in Electrical & Electronics Measurement &	J.B. Gupta	S. K. Kataria Pub. Co.			
Instrumentation					
Electronic Measurement & Instrumentation	Bell	Oxford			
A course in Electrical & Electronics Measurement &					
Instrumentation					

	End Semester Examination Scheme								
		Maxim	um Marks: 3	5			Time:	2 Hrs	
			Objective Questions				Subjective	Questions	
Group	Module	To be set	To be answered	Marks per question	Total Marks	To be set	To be answered	Marks per question	Total Marks
А	1 2	7	Any 10	1	1 x 10 =	4	Any 5 taking at least 2	5	5 x 5 = 25
В	3	6	Tiny 10	1	10	4	from each group	5	5 X 5 – 25

Syllabus for PROCESS CONTROL

Name of	the Course	: Diploma in Electronics & Instrumentation Engineering			
Name of	the Subject	: Process Control			
Subject (Code:	Semester: Third			
Duration	: 6 months	Maximum Marks: 100			
Teaching	Scheme:	Examination Scheme:			
	ry : 3 hrs/	week Internal Scheme : Teachers Assess	sment: 10		
Tutor	rial: 1 hrs/	week Class Test	: 20		
Practi	Practical: End Semester Exam				
Credit: 4					
Aim:					
Sl No.					
1		ig and control of process is the most important part of in industry. With knowledge of the vill be able to control the process parameter as per the desired value for the optimization			
Objectiv	e:				
Sl No.	The Stude	ent will able to			
1	Know bas	ics of process and control system			
2	Define the	e mathematical equation from physical system for further analysis			
3		he stability of the system			
4	Perform ti	me response analysis of different system			
5		control loop with different pneumatic & electronic controller			
6	Test, cali	brate, maintain process control elements			
7		use of controllers and control valves			
Pre-requ	isite:				
Sl No.					
1		ntal idea on instrumentation			
2		ge of mathematical calculation			
3	Knowledg	ge of basic Electronics			
		Contents			
Module	Chapter	Name of the topic	Hrs /		
110000	Chapter		Module		
	01	Introduction to Process & Control System:	12		
		1.1 Concept of process			
		1.2 Classification of process variables			
А		1.3 Concept of open loop and closed loop control system.			
11		1.4 Different elements in automatic close loop control systems.			
		1.5 Mathematical representation of simple mechanical & electrical system			
		1.6 Block diagram representation of control system, block diagram algebra. Simple problem			

	02	Stability Analysis: 2.1 Concept of transfer function.		9		
		2.1 Concept of transfer function. 2.2 Different techniques of stability a	analysis (only Name)			
		2.3 Stability analysis using Routh Hu	•	m.		
		2.4 Name of other techniques for stal				
		1				
	03	Time Response Analysis:		9		
		3.1 Introduction, standard test signals	s, performance indices.			
		3.2 First and second order system				
		3.3 Time response of first order system.3.4 Time response of 2nd order system, definition of rise time, Peak overshoot,				
		Settling time etc		ak oversnoot,		
	04	Basic Control Schemes and Controller		15		
		4.1 A basic close loop control system		M P PIPP		
		4.2 Function and explanation of foll		ff, P, PI,PD,		
		PID, Their relative merits and der				
		4.3 Explanation & transfer function of		ype pneumatic		
		controller (only motion balance ty 4.4 Explanation & transfer function of		vne electronic		
		controller		ype electronic		
		controller				
	05	Final Control Element:		15		
		5.1 Different types of On Off and reg	gulating Control Valve			
		5.2 Different parts of a Globe valve				
В		5.3 Valve actuator				
D		5.4 Explanation and use of I/P conve		gulator, Purge		
		Regulator (constatnt DP regulator				
		5.5 Idea on Control valve Characteris	stics, Cv, value selection & si	zing,		
		cavitation, flashing, rangeability				
		5.6 Specification of Control Valve 5.7 Installation of Control valve with	manifolds			
		5.8 Operation of SDV, ESDV, solen		e (pressure		
		temperature), self actuated Pressu		e (pressure,		
		5.9 Basics of valve signature.				
		-	rinciple and application of Se	ervo Motor &		
		Stepper motor	1 11			
Books:						
Title			Author	Publisher		
	Control Prin	ciple & Application	S Bhanot	Oxford University Press		
		ncept Dynamics & Application	S. K. Singh	PHI		
Principles	s of Process	s Control	D.Patranabis	Mc Graw Hill		
Instrumen	nt Engineer	s' Handbook: Process Control &	Bela G Liptak	CRC Press, Taylor &		
Optimiza	tion, Vol-II			Fraancis Group		
	Process Co	ontrol: An Introduction to Theory &	Stephanopoulos	Pearson		
	Practice D.O.J.Desa Taylor & France					
			D.O.J.Desa	Taylor & Francis		
	Control Eng	ial Process Control	K.Ogata D.P.Eckman	PHI J. Wiley & Sons		
	c Process C		D.P.Eckman	J. Wiley & Sons		
		em Engineering	Gupta	Wiley India		
		rumentation Technology	Curtis Johnson	PHI		
	c Control S		Kuo	Wiley India		
Automati	c connor s	ystem	1200	whey mula		

Modern Control System	Ogata	PHI
Control System Theory	S Dasgupta	Khanna
Process System Analysis & Control	Coughanowr	Mc Graw Hill
		International

	End Semester Examination Scheme								
		Maxim	um Marks: 7	70			Time	: 3 Hrs	
	Objective Questions					Subjectiv	e Questions		
Group	Module	To be	To be	Marks per	Total	To be	To be	Marks per	Total
		set	answered	question	Marks	set	answered	question	Marks
А	1 2	13				4	Any 5		
	3		Any 20	1	1 x 20 =20		taking at least 2	10	$10 \ge 5$ =50
В	4 5	12			-20	4	from each group		-50

Syllabus for ANALOG ELECTRONICS LAB

Name of	of the Course : Diploma in Electronics & Instrumentation Engineering						
Name of	the subject : Analog Electronics Lab						
Subject (Code:	Semester: Third					
Duration	: 6 months	Maximum Marks: 100					
Teaching	Scheme:	Examination Scheme:					
Theor	ry :	Continuous Internal Assessment					
Tutor		Performance of job : 30					
Pract	ical: 3 hrs/week	Notebook / Viva : 20					
		External Assessment					
		On spot Job : 30					
		Viva Voce : 20					
Credit: 2							
Skill to b	e developed:						
Intellect	ual Skill						
1	To locate fault in circuit						
2	Interpret the waveform						
Motor S	kill						
1	Ability to sketch circuits						
2	Ability to interpret circuit						
3							
Lint of D	List of Practical						
SI No.		Evenetimente					
<u>51 No.</u>	Experiments Experiment for input /output characteristics of BJT						
2	Experiment for input / output characteristics of BJT Experiment for input & transfer characteristics of FET.						
3		Design of RC phase shift oscillator for a given cut off frequency.					
4	Design of Wien bridge oscillator for a given of						
	· · · · · · · · · · · · · · · · · · ·						

5	Study of parameters of practical op-amp.					
6	Use of op-amp as –					
	• Noninverting amplifier.					
	• Inverting amplifier.					
	• Buffer.					
	• Adder.					
	• Differentiator.					
	• Integrator.					
	• Schmitt trigger.					
	• Triangular wave generator.					
7	Design of Low pass & High pass active filter & plotting of frequency response					
	Note: Connect the circuit on bread board and see the response on CRO. Prepare the Labsheet.					

Syllabus for DIGITAL ELECTRONICS LAB

Name of	e of the Course : Diploma in Electronics & Instrumentation Engineering						
Name of	f the subject : Digital Electronics Lab						
Subject C	Code:	Semester: Third					
Duration	: 6 months	Maximum Marks: 100					
Teaching	Scheme:	Examination Scheme:					
Theor	ry :	Continuous Internal Assessment					
Tutor	rial :	Performance of job : 30					
Pract	ical: 3 hrs/week	Notebook /Viva : 20					
		External Assessment					
		On spot Job : 30					
		Viva Voce : 20					
Credit: 2							
Skill to b	e developed:						
Intellect	ual Skill;						
1	Interpret the result						
2	Verify the result						
Motor S	kill:						
1	Accuracy of measurement						
2	Proper connection						
List of P	ractical:						
Sl No.	Experiment						
01		Study of Digital IC data sheet and noting down the characteristics for TTL & CMOS logic families					
02	Familiarization of 7400, 7402, 7404, 7408, 7432 & 7486 ICs						
03	Verification of truth tables of AND, OR, N	6					
04	Implementation of various logic gates usin	ng NAND & NOR gates (Truth table verification).					

05	Verification of DeMorgan's theorem.
06	Implementation of Adder & Subtractor using minimum number of gates.
07	Operation & verification of truth table of S-R & J-K, D, T flip-flop.
08	Design of Ripple counter (4 bit)
09	Design of a universal shift register
10	Design and application of A/D & D/A converter
11	Design of decimal to seven segment display
12	Square wave generation with particular frequency using 555 timer & analysis of output using CRO

Syllabus for PROCESS INSTRUMENTATION - I LAB

Name of	the Course : Diploma in Electronics & Instrumentation Engineering						
Name of	the subject : Process Instrumentation -I Lab						
Subject C	ode:	Semester: Fourth					
Duration:	6 months	Maximum Marks: 100					
Teaching	Scheme:	Examination Scheme:					
Theor	у:	Continuous Internal Assessment					
Tutor	al :	Performance of job : 30					
Practi	cal : 3 hrs/week	Notebook / viva : 20					
		External Assessment					
		On spot Job : 30					
		Viva Voce : 20					
Care dite 2							
Credit: 2							
Skill to be	e developed:						
Intellectu	ctual Skill;						
1	Identification & selection of equipments						
2	Selection of transducers						
Motor Sk							
1	Accuracy of measurement						
2	Proper connection						
List of Pr	List of Practical:						
Sl No.	Ex	periment					
1		serve the loading effect on output of potentiometer					
	(translation potentiometer and rotational poten						
2	Study of different parts of C type Bourdon Tube p	ressure gauge					

3	Calibration of Bourdon Tube Pressure gauge using dead weight tester.	
4	Calibration of Pressure switch using dead weight tester.	
5	Measurement of strain/force using strain gauge/Load cell	
6	Characteristics and calibration of strain gauge/Load cell	
7	Measurement of Linear displacement by LVDT & plotting of characteristics curve	
8	Characteristics and calibration of Hall effect sensor	
9	Characteristic and calibration of Piezoelectric sensor	
10	Speed measurement by contacting Tachometer	
11	Speed measurement by Stroboscope	
12	Study of Pneumatic Relay, AFR	

Syllabus for DEVELOPMENT OF LIFE SKILL II

Name of the Course : Diploma in Electronics & Instrumentation Engineering			
Name of the Subject : Development of Life Skill II			
Subject Code:		Semester: forth	
Duration:	6 months	Maximum Marks: 50	
Teaching	Scheme:	Examination Scheme:	
Theor	y :	Internal Assessment : 25	
Tutori	al :		
Practi	cal: 2 hrs/week	External Assessment : 25	
Credit: 1	Credit: 1		
Aim:	Aim:		
Sl No.			
1	In today's competitive world, the nature of organization is changing with very rapid speed. In this		
	situation the responsibility of diploma holder is not unique. He will be a part of a team and will have to		
	work as a team with others in the organization. As such the individual skills are not sufficient there to work at his best		
2	This subject will develop a student's mind to be a part of the team It will develop the abilities and skills to		
2	perform at highest degree of quality as an individual as well as a member of core group or team		
3	Such skills will enhance his capabilities in the field of searching, assimilating information, managing the		
	given task, handling people effectively, solving challenging problems.		
Objective:			
Sl No.	The Student will able to		
1	Develop the status of mind to work as a team		
2	Apply problem solving skills for a given situation.		
3	Use effective presentation techniques.		
4	Apply techniques for effective time management.		

5	Apply task management techniques for given project.	
	Enhance leadership traits.	
	Resolve conflict by appropriate method	
	Face interview without fear.	
	Follow moral and ethics.	
	Convince people to avoid frustration	
Pre-requis	site:	
Sl No.		
1	Team work and presentation skill	
2	Positive attitude and thirst of learning	
	Contents	
Module	Contents	Hrs /
Module	Name of the topic	module
1	Social Skill	
	Societies, Social Structure, Develop Sympathy and Empathy	
2	SWOT Analysis	
2	Concept, How to make use of SWOT	
3	Interpersonal Relation	
	3.1 Source of conflict, Resolution of conflict,	
4	3.2 Ways to enhance interpersonal relation Problem Solving	
4	4.1 Steps in Problem Solving	
	4.1.1 Identify and clarify the problem	
	4.1.2 Information gathering related to problem	
	4.1.3 Evaluate the evidence	
	4.1.4 Consider alternative solutions and their implications	
	4.1.5 Choose and implement the best alternatives	
	4.1.6 Review	
	4.2 Problem solving techniques	
	4.2.1 Trial and error	
	4.2.2 Brain Storming	
	4.2.3 Lateral Thinking	
5	Presentation Skills	
	5.1 Body language	
	5.2 Dress	
	5.3 Posture, Gesture, Eye contact and facial expression	
	5.4 Voice and language- Volume, Pitch, Inflection, Speed, Pause, Pronunciation,	
	Articulation, Language, Practice of speech	
	5.5 Use of Aids- OHP, LCD projector, white board	
(Commente P. Laterian Technismer	
6	Group Discussion & Interview Techniques	
	6.1 Group Discussion 6.1.1 Introduction to group discussion	
	6.1.2 Ways to carry out group discussion	
	6.1.3 Parameters- Contact, body language, analytical and logical thinking, decision	
	making	
	6.2 Interview Techniques	
	6.2.1 Appearance at interview board	
	6.2.2 Tips for handling common questions	
7	Working in Teams	
	7.1 Understanding the utility to work as a team	

7.2 Tips to work effectively in team				
7.3 Establish good rapport				
7.4 Interest toward others and work effe	ctively with them to meet com	mon objectives		
7.5 Tips to provide and accept feedback				
7.6 Leadership in team		2		
7.7 Handling frustration in team				
8 Task management				
8.1 Introduction				
8.2 Task identification				
8.3 Task planning, Organizing and Exec	ution			
8.4 Closing the task				
Student may perform the following task as				
1 SWOT Analysis Analysis yourself				
opportunities and threats. Following	points may be useful for doing	g SWOT-		
1.1 Past experience				
1.2 Achievements				
1.3 Failures				
1.4 Feedback from others				
2 Undergo a test on reading skill/ mem	ory skill administered by the t	eacher		
3 Solve the puzzles	for y skill administered by the t	caener		
-	•			
· ·	protection, distribution of cloths to poor, awareness camp like cleanliness in slump			
area etc.				
5 Deliver seminar on given topic.				
	 interest using audio / visual aids. Make a report on the programme Conduct an interview of a personality and write a report on it 			
description of the topic discussed.				
	9 Arrange an exhibition, displaying flow-charts, posters, paper cutting, photographs etc			
	on the topic given by teacher			
	Note: please note that these are the suggested assignments on the given contents/ topics. These			
	assignments are the guide lines to the subject teachers. However the subject teachers are free			
to design any assignment relevant to the topi				
Books:				
Title	Author	Publisher		
Adams Time Management	Marshal Cooks	Viva Books		
Basic Management Skills for All	E.H. Mc Grath, S.J.	PHI		
Body LAnguage	Allen Pease	Sudha Publications Pvt Ltd		
Creativity and Problem Solving	Lowe and Phil	Kogan Page (I) Pvt Ltd		
Decision making & Problem Solvong	Adair, J	Orient Longman		
Develop your Assertiveness	Bishop, Sue	Kogan Page (I) Pvt Ltd		
Make Every Minute Count	Marion E Haynes	Kogan Page (I) Pvt Ltd		
Organizational Behaviour	Steven L mcShane and	Tata McGraw Hill		
	Mary Ann Glinow	DITI		
Organizational Behaviour	Stephen P. Robbins	PHI		
Presentation Skill	Micheal Hatton	ISTE N Delhi		
Stress Management Through Yoga and meditation		Sterling Publisher Pvt Ltd		
Target Setting and goal Achievement	Richard Hale, Peter	Kogan Page (I) Pvt Ltd		
	Whilom			
Time Management Working in Team	Chakravarty, Ajanta Harding ham, A	Rupa & Company Orient Longman		

INTERNET ASSISTANCE

1. http://www.mindtools.com

- 2. http://www.stress.org
- 3. http://www.ethics.com
- 4. http://www.coopcomm.org/workbook.htm
- 5. http://www.mapfornonprofits.org/
- 6. http://www.learningmeditition.com http://bbc.co.uk/learning/courses/
- 7. http://eqi.org/
- 8. http://www.abacon.com/commstudies/interpersonal/indisclosure.html
- 9. http://www.mapnp.org/library/ethics/ethxgde.htm
- 10. http://www.mapnp.org/library/grp_cnfl/grp_cnfl.htm
- 11. http://members.aol.com/nonverbal2/diction1.htm
- 12. http://www.thomasarmstron.com/multiple_intelligences.htm
- 13. http://snow.utoronto.ca/Learn2/modules.html
- 14. http://www.quickmba.com/strategy

Note: Syllabus of Development of Life Skill II will be revised to make it common for all dept. & It will be circulated separately.

Syllabus for PROFESSIONAL PRACTICE II

Name of the Course : Diploma in Electronics & Instrumentation Engineering			
Name of the Subject : Professional Practice II			
Subject Code:		Semester: Third	
Duration:	6 months	Maximum Marks: 50	
Teaching	Scheme:	Examination Scheme:	
Theor	у:	(Only Internal Assessment)	
Tutori	al :	Continuous Internal Assessment : 30	
Practi	cal: 2 hrs/week	Viva / report / notebook etc : 20	
Credit: 1			
Aim:			
1	After passing most of the diploma holders join industries. Due to globalization and competition in the		
	industrial and service sector the selection for job is based on campus interview and competitive tests		
2	While selecting candidates a normal practice adopted is to see general confidence, attitude, ability to		
	communicate, in addition with basic technical concept.		
3	Professional practice will provide opportunity to students to undergo activities which will enable them to		
	develop confidence, attitude etc. Industrial visits, expert lecturers, seminars on technical topics and group		
	discussions are planned here so that there will be increased participation of students in learning process.		
Objective	2:		
Sl No.	The Student will able to		
1	Answer aptitude and reasoning test successfully		
2	Acquire information from different sources		
3	Prepare notes for given topics		
4	Present given topic in a seminar		
5	Interact with peers to share thought		
	Prepare a report on industrial visit, expert lectur	e	

Pre-requ	isite:		
1	Knowledge on basic electrical & electronic engineering		
2	Knowledge on Instrumentation engineering		
3	Knowledge of computer		
4	Knowledge of internet access		
	Contents		
Module	Name of the Topic / activity	Hrs / Module	
1	Aptitude and Reasoning Practice		
	1.1 General Aptitude		
	1.1.1 Arithmetic Aptitude		
	1.1.2 Data Interpretation		
	1.2 Verbal & Reasoning		
	1.2.1 Verbal Ability		
	1.2.2 Logical Reasoning		
	1.2.3 Verbal Reasoning		
	1.2.4 Non Verbal Reasoning		
2	Lecture by Professional / Industrial experts / Student Seminar based on following areas		
	(at least two)		
	·) Fig. F. Laiss (s. C.) Decess discussed Fig. All		
	a) Fire Fighting / safety Precaution and First Aids		
	b) Yoga Meditation		
	c) Problems of drinking water in rural areas		
	d) Interview techniques		
	e) Various petroleum product and its applicationf) Advancement of modern and new energy source		
	g) Green Energy Concept		
	h) Temperature reading and controlling through fiber Cable		
	i) Electrical and pneumatic transmission		
	j) Smart and Intelligent Instruments		
	k) Any other suitable topic		
	k) Any other suitable topic		
3	Group Discussion		
	The student should discuss in a group of six to eight students. Two topics (at least) for group		
	discussions may be selected by the faculty members. Some of the suggested topics are-		
	a) Road safety rules enveroness		
	a) Road safety rules awareness b) CNC us LBC as fuel		
	b) CNG vs LPG as fuel		
	c) Load shading and remedial measure		
	d) Safety in day to day life a) Energy saving in institute		
	e) Energy saving in institutef) Tuitions should be banned		
	g) Computers have resulted in unemployment		
	h) Effects of cinema/media on youth		
	i) Criticism is good or badj) Advantage of co- education		
	k) Any other suitable topic		
4	Field Visit		

Competit	ive Examinations				
	General Aptitude: Quantitative Aptitude & Reasoning for GKP GK Publisher				
	How to Crack Test of Reasoning Jaikishan, Premkishan Arihant				
	g (verbal/ Non-Verbal/ Logical)	J K Sharma	Unique Publisher		
	First Track Objective ArithematicRajesh VermaArihant				
	pproach to Reasoning Verbal & Non-verbal	B.S. Sijwali,Indu Sijwali	Arihant		
	Quantitative Aptitude for Competitive Exam R S Aggrawal S Chand				
	A modern Approach to Verbal and Nonverbal Reasoning R S Aggrawal S Chand				
	Quantitative Aptitude and Reasoning Praveen PHI				
	ive Aptitude	Mohon Rao	Scitech		
Title	• • •	Author	Publisher		
Books:					
	5.2.2.4 Presentation Notes				
	5.2.2.3 Slide Creation				
	5.2.2.2 Custom Animation				
	5.2.2 Fresentation in impress 5.2.2.1 Slide Master Slide Design				
	5.2.1.4 Frinning a presentation docume 5.2.2 Presentation in Impress	111			
	5.2.1.4 Printing a presentation docume	nt			
	5.2.1.2 Viewing a presentation docume 5.2.1.3 Inserting Picture and objects	JIII			
	5.2.1.1 Creating a presentation docume 5.2.1.2 Viewing a presentation docume				
5.2.1 Getting Started with LibraOffice Impress 5.2.1.1 Creating a presentation document					
	5.2 Introduction to LibraOffice Impress	S S			
	5.1.2.5				
	5.1.2.4 Formulas and functions				
	5.1.2.3 Advanced formatting and prote	ction			
	5.1.2.1Using Charts & graphs5.1.2.2Images and graphics				
	5.1.2 Different operations in LibraOffice Cal	c			
	5.1.1.5 Working with data				
	5.1.1.4 Basic data manipulation				
	5.1.1.3 Formatting data				
	5.1.1.2 Working with Sheets				
	5.1.1.1 Working with Cells				
	5.1.1 Getting started with LibraOffice Calc				
	5.1 Introduction to LibraOffice Calc				
5	Free & Open Source Software				
	i) Any other plant / laboratory				
	h) Paper printing plant				
	g) Water treatment plant				
	 a) Dairy plant b) Soft drink plant c) Instrument calibration laboratory d) Captive power Plant (Thermal) e) Power Generation Station f) Jute Industry 				
	The field visit may be arranged in the following areas / Industries				
	student, as part of term work.				
	Structured field visit (at least 1) be arranged ar	nd report of the same should h	be submitted by the		